## Automotive Ethernet PHY Linking The Missing Link!

# **Joint Presentation Intrepid & Keysight**

Log, Computer, Analyze and repeat, that's development





## **PHY Register Status Monitoring – Read/Write**

😅 New Spy Setup - Vehicle Spy 3 Enterprise					
<u>File</u> <u>Setup</u> Spy <u>Networks</u> <u>Measurement</u> <u>Embedded Tools</u> Scripting and <u>A</u> utomation <u>R</u> un <u>T</u> ools <u>H</u> elp					
	J Flashing	•	•   🗸 📵	🔒 🔧 🕻	Desktop 1
Ethernet PHY Dashboard	N Termina				
	eway Buil Smash	lder			
	omasn 3 Controll	er			36 Entries to allow,
		/ Dashboard			50  Littles to allow,
PHY Register	rs				Ctart/Ctar ////rita functiona
					Start/Stop/Write functions
	Write En	Device Addr	Reg Addr (Hex)	Value (Hex)	Wri
		1	03625CI	00000001	Reading status is Polled
Registers (+)		1	AE02526	0000002	
		1	625DD0	0000003	Write is sent once
Registers ( - )		1	03625EE	0000003	
Start Monitor		1	03625FE	0000004	
Stop Monitor		1	0800		
		1	0A00		1 0000 1 0000
Write Registers		1	0800		
		1	0C00		
		1	0000		1 0000 1 0000
		1	0000		
		1	0000		



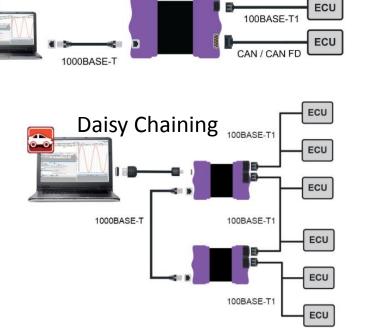


# **PHY Register R/W Access – Product List**

Ability to Read and Write PHY registers in each mode below

## **RAD-Pluto**

Ethernet ports: 4x 100BASE-T1 ports 1x 1000BASE-T ports 2x CAN FD channels with termination 1x LIN channel



Gateway Ethernet to CAN



1000BASE-T

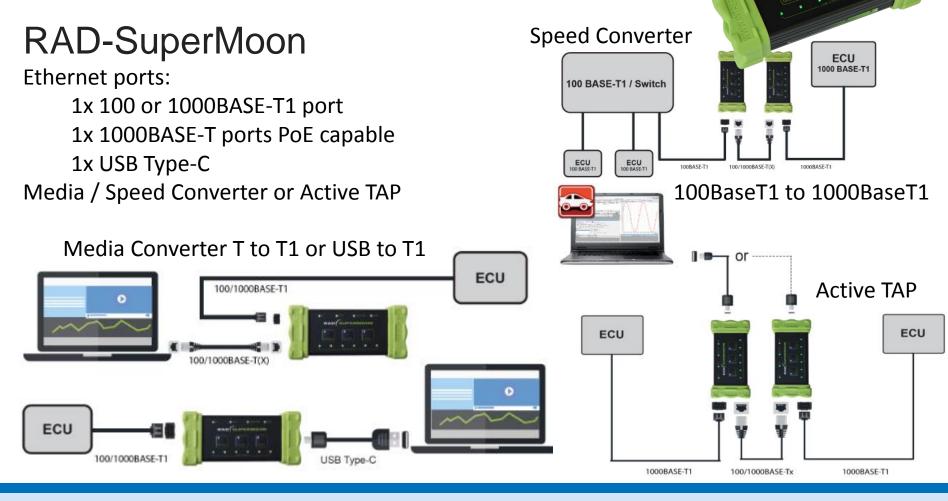






# **PHY Register R/W Access – Product List**

Ability to Read and Write PHY registers in each mode below





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# **PHY Register R/W Access – Product List**

Ability to Read and Write PHY registers in each mode below

## RAD-Moon2

Ethernet ports: 1x 100 or 1000BASE-T1 port 1x 1000BASE-T port 1x USB Type-C 1x Signal Quality LEDs

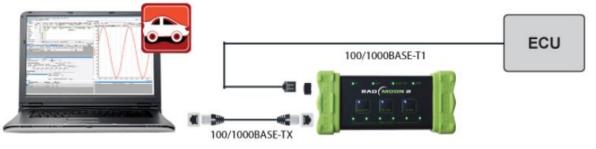


## RAD-Moon

Ethernet ports: 1x 100BASE-T1 port 1x 1000BASE-T port 1x USB Type

# Rad Raden

#### Media Converter T to T1 or USB to T1







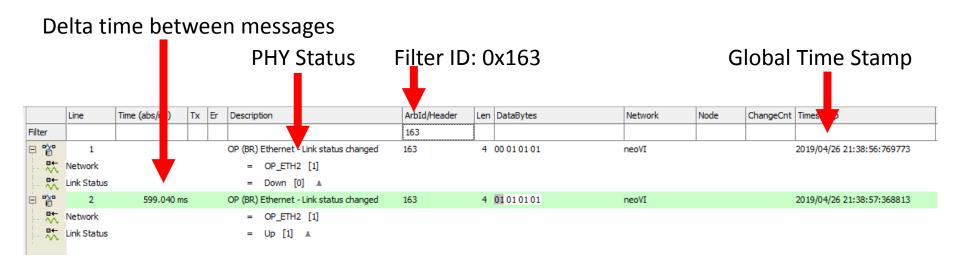
# **Polling LINK Status – Ethernet ports**

LINK Status provides:

Reports all ethernet PHY ethernet status for LINK State

Time stamp of when LINK is DOWN

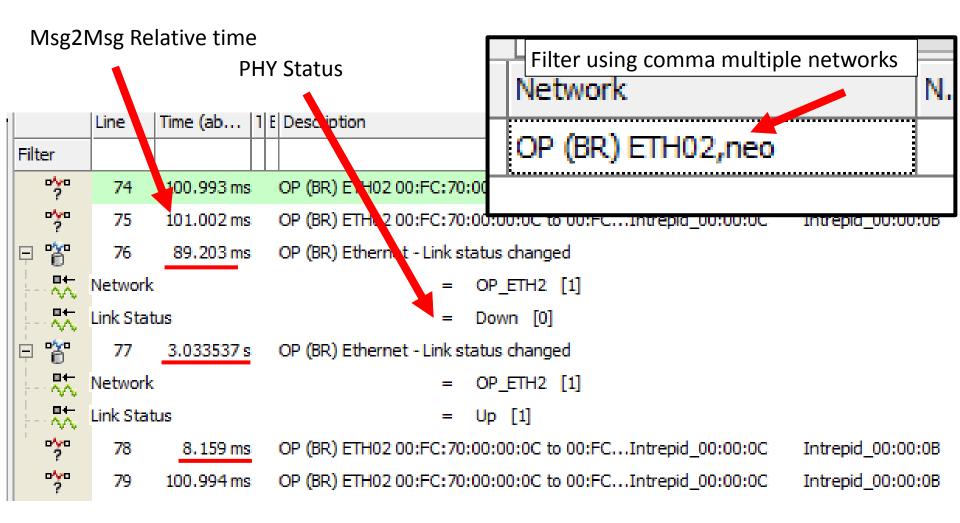
Time stamp of when LINK comes back alive (example below 600ms)







# Polling LINK Status – Ethernet ports on RX msg







# Ethernet Ports – Low Latency TAP / Converter

# Setting up the Ethernet Port Role:

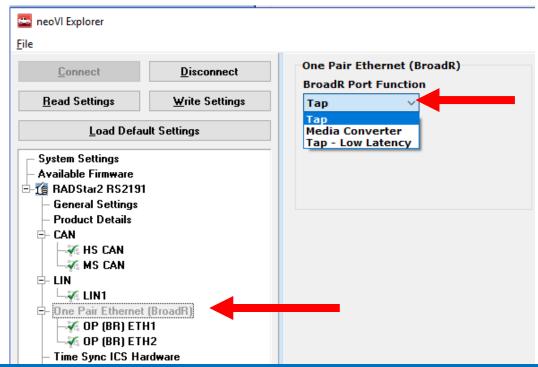
<u>Media Converter</u>

Transform from 2 wire to Classical RJ45

<u> Tap</u>

Used in-line as Pairs

<u>Low Latency</u> Cut through mode



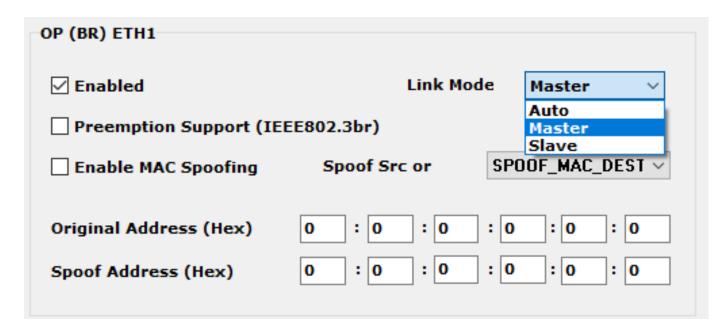




# Ethernet Ports – LINK Mode: Master/Slave/Auto

Setting up the PHYsical Layer port role: Master / Slave / Automatic can be selected

MAC Spoofing: Replace physical MAC with virtual MAC address





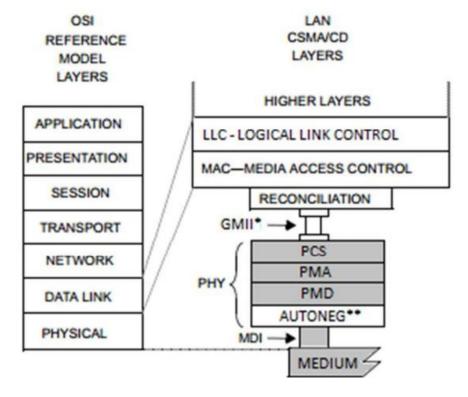
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# **PHY Linking Subsystem Stages**

### OSI Reference - MAC and LLC start Layer start at Layer2



LLC – Logical Link Control MAC – Media Access Control <u>PHY = PHYSICAL LAYER DEVICE</u> RS = RECONCILIATION LAYER GMII = GIGABIT MEDIA INDEPENDENT INTERFACE PCS = PHYSICAL CODING SUBLAYER PMA = PHYSICAL MEDIUM ATTACHMENT PMD = PHYSICAL MEDIUM DEPENDENT AUTONEG = AUTO-NEGOTIATION MDI = MEDIUM DEPENDENT INTERFACE

EEE – Energy efficient Ethernet OAM – Operations administrations management

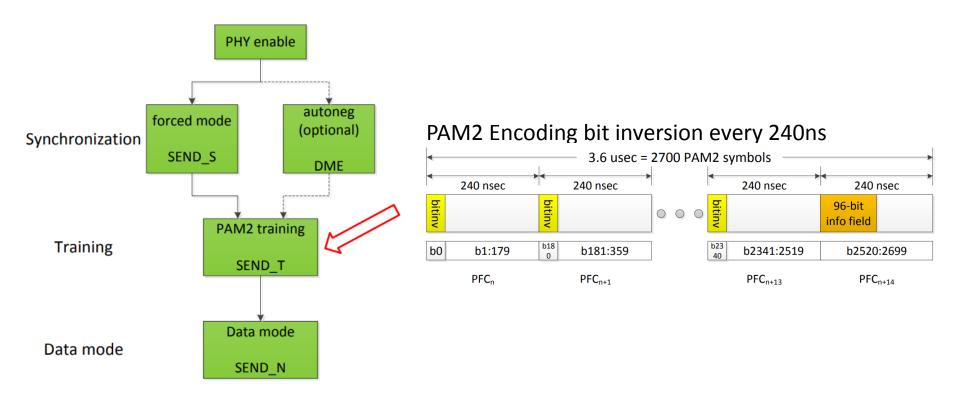
http://www.ieee802.org/3/bp/public/mar15/regev\_3bp\_01\_0315.pdf





# **PHY Linking States – Start up Sequence**

Sync, training, data mode, can be seen on a oscilloscope



http://www.ieee802.org/3/bp/public/nov14/tu\_3bp\_02b\_1114.pdf https://www.dell.com/content/topics/global.aspx/power/en/ps1q01\_hernan?c=us&l=en&cs=04



